Notice of Allowability	Application No.	Applicant(s)
	10/787,119	VOLLRATH ET AL.
	Examiner	Art Unit
	Theresa T. Doan	2814
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to		
2. The allowed claim(s) is/are <u>1-7</u> .		
<ul> <li>3.</li></ul>		
2. Certified copies of the priority documents have been received in Application No		
3. Copies of the certified copies of the priority documents have been received in this national stage application from the		
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. X CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached		
1)  hereto or 2)  to Paper No./Mail Date		
(b) ⊠ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date 20051014.		
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
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Attachment(s)	C Matter of leferous I	and Application (DTO 152)
1. Notice of References Cited (PTO-892)		atent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summary Paper No./Mail Dat 08), 7. ⊠ Examiner's Amendr	(P10-413), te
<ol> <li>Information Disclosure Statements (PTO-1449 or PTO/SB/C Paper No./Mail Date 02/27/04</li> </ol>	08), 7. ⊠ Examiner's Amendr	ment/Comment
4.   Examiner's Comment Regarding Requirement for Deposit	8. 🛭 Examiner's Stateme	ent of Reasons for Allowance
of Biological Material	9.	

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### **DETAILED ACTION**

### **EXAMINER'S AMENDMENT**

- 1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
- 2. The application has been amended as follows:

## IN THE SPECIFICATION:

The abstract of the disclosure line 1, after "... a cell array" delete "havinb" insert -having- -.

# IN THE CLAIMS:

In claim 1, line 2, a phrase of "... each **memeory** cell" delete "**memeory**" insert - **memory** - -.

In claim 1, line 3, after "... a storage capacitor" delete "andcapable" insert - - and capable - -.

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## IN THE DRAWINGS:

Figure 2 is labeled as -- PRIOR ART-- (See MPEP 608.02 (g)).

## Reasons for Allowance

3. Claims 1-10 are allowed.

The following is an examiner's statement of reasons for allowance:

The prior art of record fails to disclose all the combination recited in the base claim 1. Specifically, the combination of an integrated semiconductor circuit, comprising: a cell array, the cell array having a plurality of memory cells, each memory cell having a selection transistor and a storage capacitor and capable of being driven electrically by bit lines and word lines; in each case, at least two bit lines being connected to a common signal amplifier, wherein at least one first and a second additional word line are provided, which cannot be used for driving selection transistors, each bit line extending as far as the first or second additional word line and is connected to an additional contact structure, the additional contact structure leading which leads laterally past one of the two additional word lines and representing a dummy contact, additional contact structures of two bit lines leading past different additional word lines, in each case, the two bit lines being connected to the same signal amplifier.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Theresa T. Doan whose telephone number is (571) 272-1704. The examiner can normally be reached on Monday to Friday from 7:00AM - 4:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, WAEL FAHMY can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TD October 14, 2005.

PHAT X GAO
PRIMARY EXAMINER

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